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09/13/2013 13/2013

ISSUE CLASSIFICATION

U.S. UTILITY Patent Application

3/869

KU O.I.P.E.
SCANNED Q.A. 10

PATENT DATE

APPLICATION NO. 09/747,286 CONT/PRIOR CLASS 251 SUBCLASS 510 ART UNIT 1732 EXAMINER E. LEE

LICENTS
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Electro Gated panel with programable logic
and microprocessor manufacture thereof.

PTO-2040
12/89

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets	Drwg.	Figs.	Drwg.	Print Fig.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
				Amount Due	Date Paid
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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